

**DEVELOP:** TEAM COLLABORATION

# RAPID ONBOARDING OF NEW TEAM MEMBERS

## THE CHALLENGE

An automotive R&D team is working on advanced driver assist systems (ADAS) pedestrian detection and collision warning use cases. It is exploring field programmable gate array (FPGA) offload for computer vision (CV) algorithms.

The FPGA-based system architecture is a first for the team, which also has limited CV experience. To deliver a successful prototype ahead of internal deadlines, the team has brought on new members with FPGA and CV experience.

## THE SOLUTION

**Because the team is working in Wind River® Studio, typical challenges associated with onboarding new team members are minimized or avoided altogether.** Standardized build and test configurations and other assets are shared across subprojects and across the entire team.

Similarly, the limited FPGA development boards that the team must use are made available through Studio Virtual Lab, enabling the team to schedule and reserve targets for testing and development through a secure cloud portal or directly within VS Code, using the Studio plugin.

## THE RESULTS

Collaborating efficiently from the start, the team can deliver its prototype on time, while expanding its scope to include the latest release of a feature-builder algorithm used for object detection.



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