

VXWORKS 653 3.X MULTI-CORE EDITION **BOARD SUPPORT PACKAGES**

COURSE DESCRIPTION

The VxWorks® 653 3.x Multi-Core Edition Board Support Packages course provides engineers with a fast, cost-effective way to acquire the skills necessary to modify and extend the BSP for an integrated modular avionics (IMA) VxWorks system.

After this course, participants will be able to perform the following:

- Develop drivers for the POS
- Understand the driver initialization sequence during system boot
- Pick an appropriate IMA driver model for a VxWorks 653 ARINC system

PRODUCTS SUPPORTED

• VxWorks 653 3.x Multi-core Edition (version 3.0.1.1)

COURSE FORMAT

- This two-day, expert-led course consists of lectures and lab sessions.
- Attendees use VxWorks 653 3.x Multi-core Edition to gain experience with the topics presented.
- Participants examine and exercise simulated network topologies in hands-on labs.
- Participants receive individual guidance from an expert engineer who has extensive experience with Wind River® technologies.

AUDIENCE

- Device driver developers
- BSP developers
- System architects

PREREQUISITE SKILLS

- Experience with VxWorks 653
- Understanding of PowerPC processor architecture

PREREQUISITE COURSES

VxWorks 653.3 x Multi-core Essentials

Course title: VxWorks 653 3.x Multi-Core Edition

Board Support Packages

Duration: Two days

Instructor-led lectures and hands-on lab Format:

sessions; instructor-led Live Remote

delivery available

Content: VxWorks 653 MCE Architecture; VxWorks

> 653 MCE Boot Sequence; VxWorks 653 MCE Device Drivers; VxWorks 653 MCE Inter-Partition Communication: VxWorks 653 MCE Debug Shell; VxWorks 653 MCE Health Monitoring; POS Device Driver Integration; VxWorks 653 MCE Hardware

Monitor (optional lab)

RELATED COURSES

• VxWorks 653 3.x Multi-core Essentials

SYLLABUS

VXWORKS 653 MCE ARCHITECTURE

- Architectural design
- Platform variants
- Devices, contexts, and scheduler
- Interfaces and capabilities
- LAB: Exploring a VxWorks 653 MCE Architecture

VXWORKS 653 MCE BOOT SEQUENCE

- BSP structure
- Configuration
- Booting
- Building MOS BSP
- LAB: Exploring a VxWorks 653 MCE Boot Sequence

VXWORKS 653 MCE DEVICE DRIVERS

- Device types
- I/O model
- PAMU
- PCI
- Access to device resources
- LAB: Adding a VxWorks 653 MCE Device Driver

VXWORKS 653 MCE INTER-PARTITION COMMUNICATION

- Introduction
- Safe IPC
- MIPC
- LAB: Communicating Between Partitions with Safe IPC

VXWORKS 653 MCE DEBUG SHELL

- Debug agent
- wrdbg shell
- wrhv shell
- LAB: Using VxWorks 653 MCE Debug Shells

VXWORKS 653 MCE HEALTH MONITORING

- Health monitoring
- Adding a custom event handler
- LAB: Using a VxWorks 653 MCE Health Monitor

POS DEVICE DRIVER INTEGRATION

- Generic devices (IO model and custom driver)
- Serial devices (SIO model)
- Network devices (END model)
- LAB: Polling the Serial Device

VXWORKS 653 MCE HARDWARE MONITOR (OPTIONAL)

• LAB: Using VxWorks 653 MCE Hardware Monitoring Tools

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CONTACT US

For more information about Wind River Education Services, visit www.windriver.com/education

Wind River World Headquarters

500 Wind River Way Alameda, CA 94501

USA

Toll-free: 800-545-9463 Tel.: 510-748-4100 Fax: 510-749-2454

training@windriver.com

Wind River EMEA

Steinheilstrasse 10 85737 Ismaning Germany

Tel.: +49 89 962 445 0 Fax: +49 89 962 445 999

emea-training@windriver.com

